

REMARKS

This Submission is responsive to the Advisory Action dated October 10, 2003 and the Final Office Action dated June 20, 2003. Claims 1, 2, 9 and 14 have been canceled. Claim 15 was added. Therefore claims 3 – 8, 10 – 13 and 15 are pending.

Regarding the prior art rejections, amended claim 10 includes an “electron supplying layer” in the compound semiconductor layer. Accordingly, claim 10 is directed to an HEMT. In contrast, the cited prior art reference to **Imaizumi** only discloses a MESFET, which lacks the foregoing feature of an “electron supplying layer.” Furthermore, **Imaizumi** is entirely silent about any variation of the threshold voltage caused by the diffusion of the gate material. For at least these further reasons, the present claimed invention patentably distinguishes over the prior art.

New claim 15 further limits the intermediate layer of claim 10 to have a thickness *of* 4nm. As explained in the remarks of the September 22, 2003 Amendment (incorporated herein by reference), the thickness of the intermediate layer of the present invention is about 4nm, which is about *twice as large* as the titanium oxide layer of **Imaizumi**. Furthermore, the disclosures of **Imaizumi** do not teach or suggest the specific claimed thickness value of 4nm recited in claim 15.

If the Examiner believes that this application is not now in condition for allowance, the Examiner is requested to contact the undersigned attorney at the telephone number indicated below to arrange for an interview to expedite the disposition of this case.

Submission Under 37 C.F.R. §1.114 dated November 20, 2003
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In the event that this paper is not timely filed, Applicants respectfully petition for an appropriate extension of time. Please charge any fees for such an extension of time and any other fees that may be due with respect to this paper to Deposit Account No. 50-2866.

Respectfully Submitted,

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